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NEW QUESTION: 1

Which three statements are true concerning InfiniBand port and subnet monitoring on an X5 Database Machine?

- A. The Infiniband subnet master location can be determined by using the getmaster command run on any database server.
- B. The InfiniBand port status may be displayed on the database servers by using the LIST IBPORT command in the DBMCLI utility.
- C. The InfiniBand port status may be displayed on the Infiniband switches by using the LIST IBPORT command in the DBMCLI utility.
- D. The InfiniBand port monitoring is automatic on the database servers and is managed by Enterprise Manager.
- E. The InfiniBand port status may be displayed on the storage servers by using the LIST IBPORT command in the CELLCLI utility.
- F. The Infiniband subnet master location can be determined by using the getmaster command run on an IB switch.

Answer: B, E, F

Explanation:

Explanation/Reference:

B: From any InfiniBand switch in the network (leaf switch or spine switch), log in as root and run the getmaster command to obtain the location of the master SM as follows:

```
# getmaster
```

This command displays the host name or IP address and the IP address of the switch where the master SM is running.

C: CellCLI> list ibport - Will display InfiniBand configuration details E: The DBMCLI utility is the command-line administration tool for configuring database servers, and managing objects in the server environment.

The LIST IBPORT command displays attributes for InfiniBand ports determined by the specified attributes and filters.

References:

http://docs.oracle.com/cd/E80920_01/DBMMN/exadata-dbmcli.htm

https://docs.oracle.com/cd/E18476_01/doc.220/e18478/GUID-9FF8B5B0-3481-4B73-89D3-108CBD7EB989.htm

NEW QUESTION: 2

A number of VMs in a host are experiencing processor contention, showing down operations Which of the following host hardware upgrade paths would yield higher VM performance, for the same VMs in the given hypervisor host?

- A. Replace CPUs to increase CPU core count
- B. Replace CPUs to move from 32-bit architecture to 64-bit
- C. Replace CPUs to move from 64-bit architecture to 32-bit

D. Replace CPUs to increase current core speed

Answer: B

NEW QUESTION: 3

Which three mechanisms are used to implement MPLS TE? (Choose three.)

A. RSVP

B. CSPF

C. tunnel interface

D. MP-BGP

E. LDP

Answer: A,B,C

Explanation:

Constrained-Based Shortest Path First (CSPF).

Resource Reservation Protocol - Traffic Engineering is an extension of the resource reservation protocol (RSVP) for traffic engineering.

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